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CONFIRMATION NO. 7695

SERIAL NUMBER 10/059,176	FILING DATE 01/31/2002 RULE	CLASS 703	GROUP ART UNIT 2123	ATTORNEY DOCKET NO. OKI.298	
APPLICANTS Hirokazu Hayashi, Tokyo, JAPAN; ** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** JAPAN 160692/2001 05/29/2001 JAPAN 246268/2001 08/14/2001 IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/28/2002					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged _____ Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 7	INDEPENDENT CLAIMS 1
ADDRESS VOLENTINE FRANCOS, PLLC Suite 150 12200 Sunrise Valley Drive Reston, VA 20191					
TITLE Method for modeling semiconductor device process					
FILING FEE RECEIVED 740	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		